

FIG. 2

The state of the s

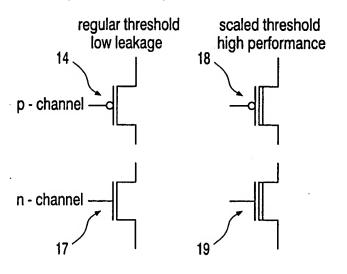


FIG. 3

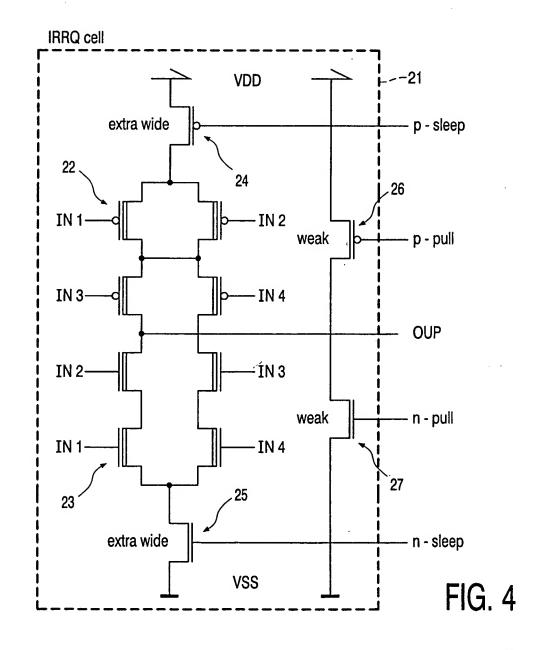


FIG. 5

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| defect to be checked  | n - netw.  | p - netw.                              | u - sleep              | IInd - u              | IInd - d         | dəəls - d                     | $_{ m I}$ ddq |
|---|--|--|------------------------|-----------------------|------------------|-------------------------------|---------------|
| within regular n - and p - channel networks   | networks   |  |                        |                       |                  |                               |               |
| n - channel open or short<br>p - channel open or short                                | stimulate<br>don't care                              | don't care                             | no                     | )<br>Jo<br>On         | yo<br>uo         | yo<br>Jyo                     |               |
| within auxiliary transistors  |  |  |                        |                       |                  |                               |               |
| n - sleep open or short<br>p - sleep open or short                                    | on<br>don't care                                     | don't care                             | toggle<br>off          | ₩<br>G                | ou<br>off        | off<br>toggle                 |               |
| p - pull open<br>p - pull short<br>p - pull on ( weak pull - up )                     | on<br>on<br>on                                       | don't care<br>don't care<br>don't care | toggle<br>toggle<br>on | # di                  | yo<br>Uo<br>Jo   | jjo<br>jjo                    | check         |
| n - pull open<br>n - pull short<br>n - pull on ( weak pull - dn )<br>or alternatively | don't care<br>don't care<br>don't care<br>don't care | on<br>on<br>on<br>don't care           | off<br>off<br>off      | 9<br>9<br>9<br>9<br>9 | off<br>off<br>on | toggle<br>toggle<br>on<br>off | check         |

FIG. 6

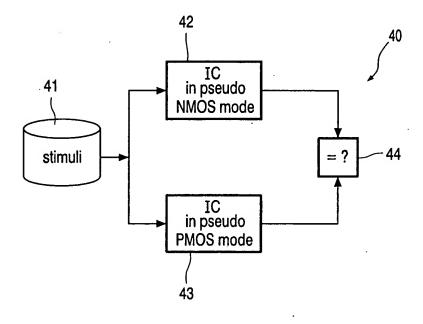


FIG. 7A

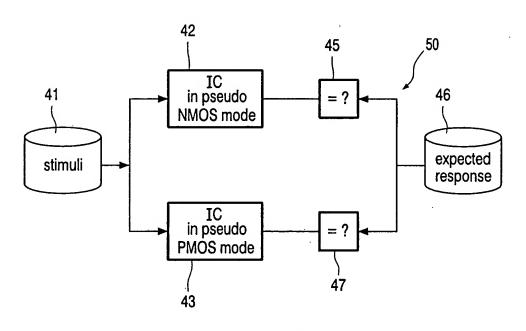


FIG. 7B